

## Refine Search

10/606,670

### Search Results -

Terms	Documents
L3 and (EEPROM or EPROM or antifuse or flash or SRAM)	8

Database:

US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

Search:

L4





### Search History

DATE: Saturday, September 25, 2004    [Printable Copy](#)    [Create Case](#)

#### Set Name Query

side by side

*DB=USPT; PLUR=YES; OP=ADJ*

#### Hit Count Set Name

result set

<u>L4</u>	L3 and (EEPROM or EPROM or antifuse or flash or SRAM)	8	<u>L4</u>
<u>L3</u>	L2 and optical and (reflect or reflective or reflectiveness)	13	<u>L3</u>
<u>L2</u>	L1 and laser and interconnections	184	<u>L2</u>
<u>L1</u>	(pld) or (programming adj logic adj device)	4489	<u>L1</u>

END OF SEARCH HISTORY

## Hit List

Clear

Generate Collection

Print

Fwd Refs

Bkwd Refs

Generate OACS

### Search Results - Record(s) 1 through 8 of 8 returned.

☐ 1. Document ID: US 6706402 B2

L4: Entry 1 of 8

File: USPT

Mar 16, 2004

US-PAT-NO: 6706402

DOCUMENT-IDENTIFIER: US 6706402 B2

TITLE: Nanotube films and articles

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw: De
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☐ 2. Document ID: US 6624524 B1

L4: Entry 2 of 8

File: USPT

Sep 23, 2003

US-PAT-NO: 6624524

DOCUMENT-IDENTIFIER: US 6624524 B1

TITLE: Laser alignment target

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw: De
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☐ 3. Document ID: US 6333940 B1

L4: Entry 3 of 8

File: USPT

Dec 25, 2001

US-PAT-NO: 6333940

DOCUMENT-IDENTIFIER: US 6333940 B1

TITLE: Integrated digital loop carrier system with virtual tributary mapper circuit

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KWIC	Draw: De
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☐ 4. Document ID: US 6049550 A

L4: Entry 4 of 8

File: USPT

Apr 11, 2000

US-PAT-NO: 6049550

DOCUMENT-IDENTIFIER: US 6049550 A

TITLE: Integrated digital loop carrier system with virtual tributary mapper circuit

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMMC	Draw De
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☐ 5. Document ID: US 6002182 A

L4: Entry 5 of 8

File: USPT

Dec 14, 1999

US-PAT-NO: 6002182

DOCUMENT-IDENTIFIER: US 6002182 A

TITLE: Laser alignment target

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMMC	Draw De
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☐ 6. Document ID: US 5998295 A

L4: Entry 6 of 8

File: USPT

Dec 7, 1999

US-PAT-NO: 5998295

DOCUMENT-IDENTIFIER: US 5998295 A

TITLE: Method of forming a rough region on a substrate

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMMC	Draw De
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☐ 7. Document ID: US 5784377 A

L4: Entry 7 of 8

File: USPT

Jul 21, 1998

US-PAT-NO: 5784377

DOCUMENT-IDENTIFIER: US 5784377 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Integrated digital loop carrier system with virtual tributary mapper circuit

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMMC	Draw De
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☐ 8. Document ID: US 5185736 A

L4: Entry 8 of 8

File: USPT

Feb 9, 1993

US-PAT-NO: 5185736

DOCUMENT-IDENTIFIER: US 5185736 A

TITLE: Synchronous optical transmission system

Full	Title	Citation	Front	Review	Classification	Date	Reference			Claims	KMC	Draw D
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Terms	Documents
L3 and (EEPROM or EPROM or antifuse or flash or SRAM)	8

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